

PHOTO VOLTAGE GENERATION PILE UP AND DISCHARGE CHIPS DEVICE NO. : OTC-121A

1. Scope :

- 1-1. This specification applies to photo voltage generation pile up and discharge chips, Device No. OTC-121A
- 1-2. This chip used as Photo Mos relay application.

2. Structure :

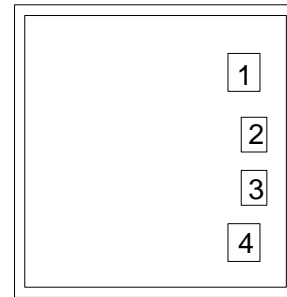
- 2-1. Planar type .
- 2-2. Electrodes : Output : Aluminum Alloy
Ground : Aluminum Alloy

3. Size :

- 3-1. Chip size : 60 mils × 60 mils (1.520 mm × 1.520 mm).
- 3-2. Chip thickness : 12±1.5 mils (0.305 ± 0.038 mm).
- 3-3. Pad size : Output : 6.2 mils × 7.1 mils (0.157 mm × 0.180 mm).
Ground : 5.0 mils × 7.1 mils (0.125 mm × 0.180 mm).
- 3-4. Pattern drawing : Refer to the attached drawing.

4. Electrical characteristics (Ta = 25 °C)

PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX.	UNIT
Forward Voltage	V_{FD}	$I_{FD}=50\mu A$	5	6		V
Open Voltage	V_{OC}	* $I_F=5mA$		5.4		V
Short Current	I_{SC}	* $I_F=5mA$		2.8		μA
Turn-ON time	T_{ON}	* $I_F=10mA$		0.3	1.0	ms
Turn-OFF time	T_{OFF}	$C_L=100PF$		0.2	1.0	ms



PAD 1,4 : Output
PAD 2,3 : Ground

* OTC121A and IRLED package together.

5. Test circuits

