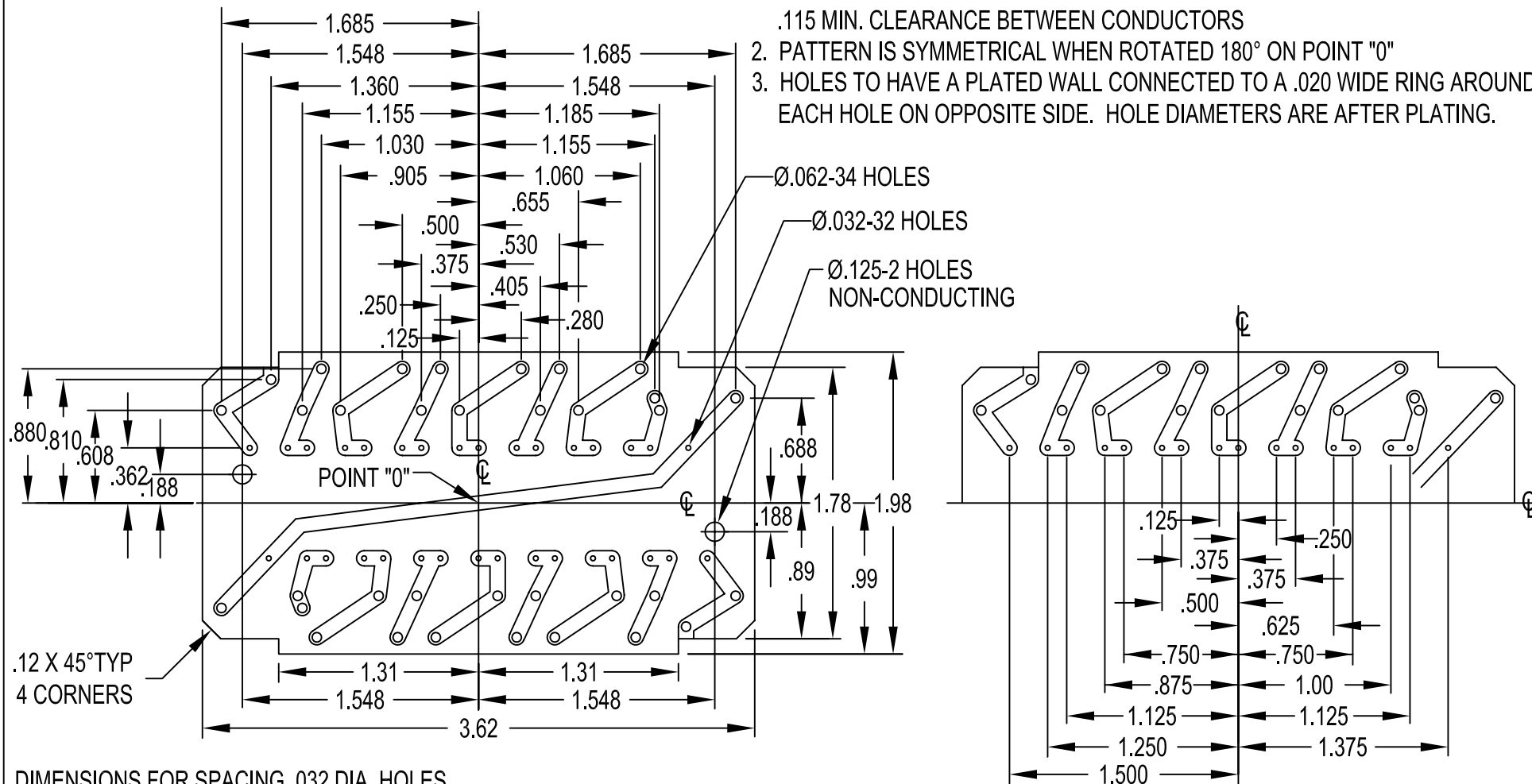


- NOTES: 1. CENTER .100 WIDE CONDUCTING AREA ON HOLES. TRACES .100 WIDE
 .115 MIN. CLEARANCE BETWEEN CONDUCTORS
 2. PATTERN IS SYMMETRICAL WHEN ROTATED 180° ON POINT "0"
 3. HOLES TO HAVE A PLATED WALL CONNECTED TO A .020 WIDE RING AROUND EACH HOLE ON OPPOSITE SIDE. HOLE DIAMETERS ARE AFTER PLATING.



DIMENSIONS FOR SPACING .032 DIA. HOLES

MATERIAL: BASE BOARD: .062 THK LAMINATED PLASTIC GLASS EPOXY GRADE FR-4 CONDUCTING AREA = 2 OZ/IN ² CU. LEAD TIN COATED
FINISH:
REVISIONS:

CKE

LUCERNEMINES, PA 15754
(724) 479-3533

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TOLERANCE UNLESS NOTED:	
DECIMAL	+/-0.005
FRACTIONAL	+/- 1/64
SCALE: FULL	
DRAWN: DEG	DATE: 11/13/01
APPROVED:	DATE:

TITLE: PRINTED CIRCUIT BOARD FOR S2 AND S3 HV SILICON RECTIFIERS
DRAWING NO. 34-2240