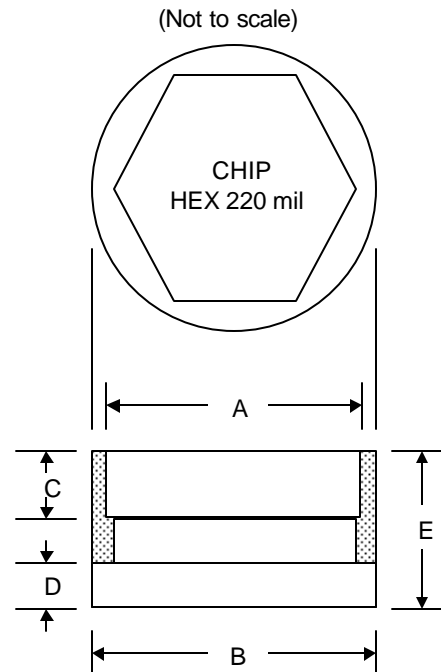


C50J		
Dim	Min	Max
A	—	6.55
B	7.20	7.40
C	—	1.0
D	—	0.75
E	2.0	2.4
All dimension in mm		

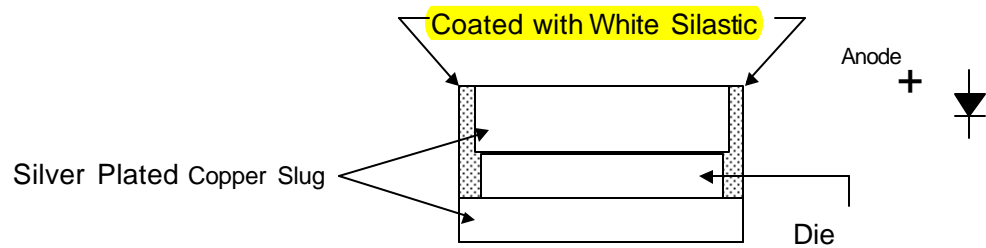


Rating and Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	C50J	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	600	V
RMS Reverse Voltage	$V_{R(RMS)}$	420	V
Average Rectified Output Current @ $T_C = 125^\circ\text{C}$	I_o	50	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	500	A
Peak Forward Voltage Drop @ $I_F = 80\text{A}$	V_{FM}	1.0	V
Peak Reverse Leakage Current At Rated DC Blocking Voltage @ $T_A = 25^\circ\text{C}$	I_{RM}	3.0	A
Operating and Storage Temperature Range	T_J, T_{STG}	-40 to +150	°C

Mechanical Data

- Case: Protect with Silicon Rubber
- Terminal: Slug, Cu with Ag Plated
- Polarity: Indicated by Large Slug on Cathode Side
- Chip: Glass Passivated Die Construction
- Solder Preform: 95Pb / 5Sn



Note:

1. Corners of chip not extend beyond electrode.
2. Top and bottom surfaces free of residue.